Preamble

The National Mission on Power Electronics Technology (NaMPET) is a national mission programme launched in November 2004 by the Department of Information Technology (DIT) under Ministry of Communication and Information Technology, Government of India. It is a pioneering endeavour to galvanize and streamline the academic, research, engineering and entrepreneurial activities within India in the area of Power Electronics Technology. Principal objective of NaMPET is to enable Indian Power Electronics Industries to attain a dominant position in the international market. A National Steering Committee comprising of eminent experts from different agencies and institutions in India guides the activities of NaMPET and WPEE-III is one of such activities.

The Workshop on Power Electronics Education-III (WPEE-III) aims to bring together experts from academic institutions to share their views in the following key directions.
1. Restructuring of Power Electronics curriculum at BE and PG level and suggest suitable course structure to the universities.
2. Preparing a list of a set of laboratory equipment, and prototype suitable lab experiments to supplement the curriculum.
3. Introducing several specialized course on advanced Power Electronics topics such as a) Power Quality b) AC Drives c) Digital control of PE systems and so on.

Registration

There is no registration fee. Invited delegates will be paid travel allowances as per Government of India norms. Local transport and hospitality will be provided by NIT Rourkela. However, delegates should inform their date and time of arrival at Rourkela and departure from Rourkela, well in advance.
Advisory Committee

Prof. S. K. Sarangi
Director, NIT Rourkela

Dr. V. K. Aatre
Chairman, National Steering Committee, NaMPET

Shri. T. K. Sarkar
Sr. Director, DIT, Govt. of India

Shri. Rajan T Joseph
Executive Director, CDAC, Thiruvananthapuram

Prof. H. P. Khincha
Vice Chancellor, VTU

Prof. S. Sengupta
Vice Chancellor, WBUT

Prof. V. Ramanarayanan
EE, IISc, Bangalore

Dr. Praveen Vijayaraghavan
GM, IEC, Bangalore

Prof. B. Umamaheshwari
Anna University, Chennai.

Organizing Committee

Mrs. K. A. Fathima, CDAC, Thiruvananthapuram
Mr. V. S. Suresh Babu, CDAC, Thiruvananthapuram
Prof. V. T. Ranganathan, EE, IISc, Bangalore
Prof. B. D. Subudhi, HOD, EE, NIT Rourkela
Prof. K. B. Mohanty, Coordinator
Prof. B. Chitti Babu, Co-Coordinator
Prof. P. C. Panda, NIT, Rourkela
Prof. J. K. Satapathy, NIT, Rourkela
Prof. S. Rauta, NIT, Rourkela
Prof. A. K. Panda, NIT, Rourkela
Prof. K. K. Mohapatra, NIT, Rourkela
Prof. S. R. Samantaray, NIT, Rourkela
Prof. Sharmili Das, NIT, Rourkela
Prof. Sandip Ghosh, NIT, Rourkela
Prof. Susovan Samant, NIT, Rourkela

Workshop Schedule

13th July 2009
08.30 a.m. : Registration
09.30 a.m. : Inauguration
11.00 a.m. - 5 p.m. : Technical Discussions

14th July 2009
09.30 a.m. - 12.30 p.m. : Panel Discussion
2.00 p.m. - 5.00 p.m. : Concluding Session

WPEE- III
13th – 14th July 2009

REGISTRATION FORM

Name:
Designation:
Organization:

Official Address:

Mobile/Telephone No:
E-mail:

Date and Time of arrival:
Date and Time of leaving:

Date
Signature

Address for Correspondence

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