



BIS STUDENT CHAPTER ACTIVITY

Workshop on PCB Layout & Design: BIS Standards, Awareness for Future Engineers



Speaker:

Mr. Senasinti Aloka Patro,
Director, Apstronics Technologies Pvt. Ltd.



8th December 2025



10.00 AM -12:30 PM

03.00 PM -05:30 PM



Venue: Simulation LAB-I, EE-308

COURSE CONTENTS

- Introduction to Printed Circuit Board (PCB) and its Design
- Overview of BIS and IEC Standards
- Schematic Design Fundamentals
- PCB Layout Fundamentals
- Design Rules and Best Practices
- Hands-On PCB Design
- Manufacturing File Generation
- Practical Soldering Session

scan to register:



Coordinator:

Prof. Monalisa Pattnaik
Mentor , BIS Student Chapter

Organized By:

BIS Student Chapter
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