



# NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA

[An Institute of National Importance under Ministry of Education, Govt. of India]

Sector 1, Rourkela, Sundergarh Dist., Odisha 769 008

## SPONSORED RESEARCH INDUSTRIAL CONSULTANCY CONTINUING EDUCATION

NITR / SR / 2026 / Advt.- 26EE018 / L / 035

dt.02-Jun-2026 Ref: FTS 260601-7970

**SR-R03**

### Advertisement for Recruitment of Project Position

Candidates who fulfill the below mentioned criteria may submit the application form before the last date. Engagement will be purely on temporary / contractual basis and co-terminus with the completion of the project. Candidates are advised to go through the advertisement details carefully before applying.			
1.	Title of the Project:	Development of single stage boost inverter with intelligent controller for rooftop PV Application.	
2.	Project Code, Dept. with Closing Date:	SR-26-EE-018	31-Oct-2027 Electrical Engineering
3.	Funding Agency Details:	IIT PALAKKAD TECHNOLOGY IHUB FOUNDATION (IPTIF) -	
4.	PI details:	Prof. Manoranjan Sahoo	sahooma@nitrrkl.ac.in, 0661-246 2432 - @ gmail.com
5.	Details of the Post(s):		
Junior Research Fellow (JRF)		6. Educational Qualification & Working Knowledge	
[Name of the Post(s)]		<b>Essential Qualification (s):</b>	
01 (One)	01 Year(s), 00 Month(s)	The candidate must have obtained First Class with a minimum of 60% aggregate marks or a CGPA of at least 6.5 on a 10-point scale in both:	
[No. of Post(s)]	[Tenure of Post]	1. B.Tech/BE in Electrical Engineering/ Electrical and Electronics Engineering	
Year 1 & 2		2. MTech/ME in Power Electronics/ Power Electronics and Drives	
INR 37,000.00 /- per month (+) HRA @ NA % (if applicable)		<b>Desirable Qualification:</b>	
- NA -		1. Candidate should have experimental knowledge in the hardware/control design of power converters.	
INR 0.00 /- per month (+) HRA @ NA % (if applicable)		2. Familiarity with MATLAB, PLECS, PSIM, PCB Design Software and DSP/FPGA Controller.	
<b>Job Description:</b>			
To carryout research work in design and development of Power Electronics converter (experimental and simulation) for renewable energy application and its control.			
Notified Later	Interview details:	-	Department of Electrical Engineering
<b>Application link for eligible candidate(s):</b> <a href="#">NIT Rourkela Homepage</a> ⇒ <a href="#">FACULTY &amp; STAFF</a> ⇒ <a href="#">SRICCE</a> ⇒ <a href="#">Career</a> ⇒ <a href="#">Notices</a>			
The candidate(s) are required to send the complete filled and signed application (soft copy) with documents regarding educational qualification indicating percentage of marks / division (mark-sheets and / or certificates), research papers (if any), work experience certificate (if any) etc., This may be built as a single PDF file and sent by email with "Advertisement No." on the subject link to the above mentioned e-mail IDs. NO hard copies of application(s) are required to be sent to the Institute. <b>Last date for submitting the Application: 21-Jun-2026</b>			
The period of experience in a discipline / area of work, wherever prescribed, shall be counted after the date of acquiring the minimum prescribed educational qualifications for that position. Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project. Selection / Joining will be cancelled in case of any suppression of information / document submitted.			
NIT Rourkela reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post. Only short-listed candidates will be informed for Online interview. In case, any clarification is required on eligibility regarding the above post, the candidate may contact in the above mentioned details.			
<b>Age Guideline:</b> The upper age limit for applying for the award of project position shall be 30 years, which is relaxed up to 5 years in the case of candidates belonging to Schedule Castes / Schedule Tribes / PWD and Female applicants whereas 3 years in the case of OBCs (Non-creamy layer candidates). Upper age limit shall be reckoned as on the last date of receipt of applications.			
<b>Any other terms &amp; conditions governed as per guidelines issued by the funding agency for the engagement of above position as amended from time to time shall be in force towards this temporary recruitment.</b>			
<b>Age relaxation will be as per norms and guidelines of the Institute.</b>			
			<b>Sd/-</b>
			<b>Asst. / Dy. / Jt. Registrar (SR)</b>
<b>Copy to:</b>			
➤ PI: Prof. Manoranjan Sahoo, EE ➤ Chairman, DRC, Dept. of EE ➤ Advertisement File			
➤ Head of the Department / Centres / Units (It is requested that the contents of the above advertisement be brought to the notice of the staff(s) / student(s) working in your Deptt. / Centre / Unit.)			
➤ Dealing Seat (SR – Project Recruitment) → <u>To publish advertisement at NITR website.</u>			