

Application Form

(Tick the appropriate Boxes)
(FILL IN CAPITAL LETTERS)

| | | | | |
|---|--------------------------|--------------------------|--------------------------|------------------------------------|
| Name: | | | | |
| Male | | Female | | |
| Category: | | | | |
| Student | <input type="checkbox"/> | Teacher | <input type="checkbox"/> | Sponsored <input type="checkbox"/> |
| College / Organization's Name: | | | | |
| Address for Correspondence: | | | | |
| Phone/Mobile: | | | | |
| Email: | | | | |
| Draft No: | | Branch name: | | |
| Amount: | | Date: | | |
| Educational Qualification | | | | |
| Highest Degree: | | | | |
| College Name: | | | | |
| Subject / Specialization: | | | | |
| Grade / Division: | | | | |
| Accommodation Required | NO | YES | Hostel | Visitor Hostel |
| | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Forwarding remark of the Head of the Department / Institute | | | | |
| Signature (with seal) | | | | |

Mailing Address:

K.K.Mahapatra
Dept. of ECE
National Institute of Technology
Rourkela-769 008, INDIA.
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Email: vlsi.nitrkl@gmail.com

Note

Please mention on the envelope

**"APPLICATION FOR TRAINING PROGRAM ON CMOS
DIGITAL IC DESIGN"**

A Short Term Course on CMOS DIGITAL IC DESIGN



Coordinator
K.K.Mahapatra

Co-coordinator
B.D.Sahoo

7th – 18th May 2007



Dept. of ECE
National Institute of Technology
Rourkela-769 008.

Course Objective

Digital CMOS have been the driving force behind Very Large Scale Integration (VLSI) for high-performance computing and other scientific and engineering applications.

The main objective of this course is to acquaint participants with VLSI Design fundamentals and provide them with practical knowledge of the software and tools used for these VLSI designs.

CMOS DIGITAL IC DESIGN

- ◆ VLSI design Overview
- ◆ Back-End Design
- ◆ CMOS fabrication process
- ◆ CMOS logic gates
- ◆ Sub-system design
- ◆ CMOS layout design
- ◆ Front-End Design
- ◆ High level design flow and HDL
- ◆ Synthesis
- ◆ VHDL

Acquaintance with Digital Electronics would be of great help.

Software and Tools to be used:

- ◆ Tanner tools: Tanner-Spice
- ◆ ISE Xilinx9.1i, Mentor Graphics, Synopsys
- ◆

Information

Intended Audience

The course is designed primarily for scholars who would like to further their education in VLSI Design. Professionals working in these fields, as well as Students of Electronics, Electrical and Computer Science & Engineering would find this course extremely useful and interesting.

FEES:

Student: Rs. 5,000/-
Faculty: Rs. 6,000/-
Industry: Rs. 8,000/-

Mode of Payment:

Payment should be done in DD in favor of "Continuing Education, NIT Rourkela" payable at **SBI, NIT Campus Branch**. (Code: 2109).

Accommodation:

Accommodation will be provided in Students' hostel or Visitors' hostel of NIT, Rourkela as per availability. For hostel & Visitor Hostel room rent will be charged as per actual.

Important Dates:

| | |
|--|-------------------|
| Last date of receipt: (Completed application form with Draft) | 30/04/2007 |
| Selection letters to be mailed: | 30/04/2007 |
| Course Commences on: | 07/05/2007 |

For further details please contact:

K.K.Mahapatra: 0661-2462454(O)
: 0661-2464458(VLSI Lab)
: 0661-2464459(ESD Lab)

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Website

http://www.nitrkl.ac.in/cmos_digital_ic_design.html