# Dr. Sarat Kumar PATRA

Professor & Director, Indian Institute of Information Technology Vadodara C/o - Block No.9, Govt Engineering College, Sector - 28, Gandhinagar, Gujarat - 382028 DOB - 14 July 1964

# **COMMUNICATION DETAILS**

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G-SCHOLAR: Citations:1526, h-Index:17, i10-Index:46

R-GATE: ResearchGate

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#### MEMBERSHIP OF PROFESSIONAL SOCIETIES

Senior Member, IEEE (40028516); Fellow, IETE (F060997); Fellow, IE (F1287594); Life Member, CSI (00071725); Life Member, ISTE (05050).

#### **EDUCATION**

1998	PhD (ELECTRICAL ENGINEERING), University of Edinburgh, Edinburgh, UK			
	(Through Commonwealth Scholarship)			
	Thesis: "Development of Fuzzy Based Channel Equalizers"			

1992 M.Sc (ENGINEERING), Specialization in ELECTRONICS SYSTEMS AND COMMUNICATIONS

National Institute of Technology, Rourkela,(formerly "Regional Engineering College")

Odisha

Thesis: "Development of Neural Network based Adaptive Equalizers"

1986 B.Sc (ENGINEERING), in ELECTRONICS AND TELE-COMMUNICATION Engineering Veer Surendra Sai University of Technology, formerly "University College of Engineering (UCE)", Sambalpur, Odisha

#### **WORK EXPERIENCE**

Employer	Post Held	Pay Scale	Period of Employment	
			(From)	То
IIIT Vadodara	Prof & Director	Level 17 (7 <sup>th</sup> CPC)	05-08-2017	Till Date
NIT Rourkela	Professor	PB-IV, AGP-10,000	01-07-2006	04-08-2017 (On Lien)
NIT (REC) Rourkela	Lecturer to Associate Prof.	2200 to PB-IV, AGP- 9000	16-3-1989	30-6-2006
DRDO	Scientist-B	2200-4000	29-9-1986	15-3-1989

# RESEARCH PROJECT UNDERTAKEN

- 1. Development of a Hand Gesture Based Interface System for Remote Controlled Surveillance Mobile Robot, (Rs. 28 Lakhs, 2016, DRDO, Completed).
- 2. Consequence analysis and risk assessment for radiation hazards at Integrated Test Range, Chandipur, India, (Rs. 9.2 Lakhs, 2012, ITR-DRDO, Completed).
- 3. FPGA based standalone tunable Fuzzy Logic Controller module, (Rs. 28 Lakhs, 2011, BRFST- Ahmedabad, Completed).
- 4. CARS project on Design and testing of BASE-BAND and RF front end for IEEE802.15.4 Zigbee standard, (Rs. 7.5 Lakhs, 2010, Anurag-DRDO, Completed).
- 5. CARS project on Development of Data acquisition system for OTO-Melara naval gun, (Rs. 7.8 Lakhs, 2010, PXE-DRDO, Completed).
- 6. CARS project on setting up comprehensive data network system for transmission of video, voice data synchronized with time, (Rs. 9 Lakhs, 2009, PXE-DRDO, Completed).

- 7. National mission project on education through ICT, development of suitable pedagogical methods for various classes of intellectual caliber research in e-learning course development for mobile communications, (Rs. 10 Lakhs, 2009, MHRD, Completed).
- 8. Development of Mobile Communication Receiver algorithms on Embedded systems using Real Time Operating System, (Rs. 12 Lakhs, 2005, MHRD, Completed).
- 9. Development of Adaptive Fuzzy System Based Receivers for Direct Sequence Code Division Multiple Access (DS-CDMA) Communication Systems, (Rs. 7 Lakhs, 2002, MHRD, Completed).

# **CONSULTANCY PROJECT**

1. Technical and Functional Testing of the customized GAGAN Enabled GPS PDAs of forest department, (Rs. 1.78 Lakhs, 2015, Govt. of Odisha, Completed).

## **ADMINISTRATIVE RESPONSIBILITY**

- Director, IIIT Vadodara (Aug 2017 Contd).
- Institute's GIAN Coordinator (2015 2017, till leaving to join IIIT Vadodara).
- Chairman (Curriculum Development Committee), NIT Rourkela (Jul 2016 2017).
- Chairman (Accreditation Ranking), NIT Rourkela (Jul 2015 Jun 2017).
- Member of BOG, NIT Rourkela (2014-2016).
- Member of Building and Works Committee; NIT Rourkela (2014-2016).
- Dean (Faculty Welfare) NIT Rourkela (Jul 2012 Jun 2015).
- Chairman (Computer and Networking Purchase Committee), NIT Rourkela (Jul 2012 to till 2017).
- HOD, Dept of Electronics Communication Engineering, NIT Rourkela (Jul 2008 Jun 2011).
- HOD, Computer Centre, NIT Rourkela (Jul 2005 Jun 2008 & Jul 2011 Jun 2012).

## FOREIGN UNIVERSITY COLLABORATION

• University of Cape Town, South-Africa (2012), Czech Technical University Prague, Czech Republic (2013), Mondragon University Spain (2014), Instituto Superio Tecnico (IST), University of Lisbon, Portugal (2015).

## THESIS SUPERVISED

- PhD Thesis Supervised: Awarded = 17, Under Progress = 3
- M.Tech (R) Thesis Supervised: Awarded = 7
- M.Tech Project (Thesis) Supervised:  $\geq 50$

# REVIEWER OF JOURNALS PUBLISHED BY:

IEEE, IET, Taylor and Francis, National Academy of Science, IETE (Taylor and Francis), Springer: Photonics and Network Communication.

#### **PUBLICATIONS**

• Book Published: 3

• Publication in Book Chapter: 6

• Publication in Journals: 72<sup>+</sup>

• Publication in Conferences: 115<sup>+</sup>