



NATIONAL INSTITUTE OF TECHNOLOGY, ROURKELA – 769 008(ODISHA)
SPONSERED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION

Advt. No.: **NITR/SR/ME-002/315(A)-Extn**

Date: **05/10/2018**

Applications are invited on prescribed format for the temporary post of "**Junior Research Fellow (JRF)**" under the IMPRINT project (Project No. 6292, **Design and Development of Dynamic Mechanical Analyser**) jointly sanctioned by MHRD (Govt. of India) and SERB (DST, Ministry of Science and Technology, Govt. of India).

1.	Name of the Temporary Post	Junior Research Fellow (JRF) – 01
2.	Name of the Research Project	Design and Development of Dynamic Mechanical Analyser
3.	Name of the Sponsoring Agency	MHRD (Govt. of India) and SERB (DST, Ministry of Science and Technology, Govt. of India)
4.	Tenure of the Project	03 Year
5.	Tenure of the Assignment	Extendable annually till completion of project, subject to satisfactory performance and availability of fund release by sponsoring agencies
6.	Job Description	JRF is required for the IMPRINT project (GOI funded) a. Circuit simulation b. Micro-controller, FPGA/ DSP based embedded system fabrication and programming (machine coding) c. Development and fabrication of surface mounted PCB's
7.	Reporting to	Principal Investigator and Co - Principal Investigator
8.	Consolidated monthly compensation / Fellowship	Rs. 25,000/- per month
9.	Essential Qualifications & experience	M.Tech/ M.E. (after B.Tech/ B.E.) in Electronics/ Electronics and Electrical/Electrical Engineering (OR) Graduate Degree (BE/B.Tech) in Electronics/ Electronics and Electrical/Electrical Engineering with valid GATE Score above the prescribed cut off level/NET Qualification
10.	Desirable Qualifications & experience	Prior knowledge of a. Circuit simulation b. Micro-controller, FPGA/ DSP based embedded system fabrication and programming (machine coding) c. Development and fabrication of surface mounted PCB's
11.	Accommodation	Bachelors accommodation in the Institute may be provided subject to availability.

For technical information on the project, the candidate may contact the Investigators at the following addresses:

Name	Prof. Tarapada Roy (PI)	Prof. Somnath Maity
Department	Mechanical Engineering	Electrical Engineering
E-mail	tarapada@nitrkl.ac.in	somnathm@nitrkl.ac.in

Eligible persons may apply in the prescribed format ([http://nitrkl.ac.in/OldWebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64\(2\).pdf](http://nitrkl.ac.in/OldWebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf)) affixed with colored photographs to be submitted in duplicate along with photo copies of relevant certificates, grade/ mark sheets, publications etc., to **Asst. Registrar, SRICCE, National Institute of Technology, Rourkela-769 008** on or before **24.10.2018**. The cover should be super- scribed clearly the post applied for & Name of the Project. Shortlisted candidates will be informed for the date of interview.

NB:

1. Mere possession of minimum qualification does not guarantee invitation to the interview.
*Candidates will be short listed based on merit and need of the project.
2. Those who have applied against advertisement no: **NITR/SR/ME-002/315(A)** dated:08/08/2018 need not apply again.

**Sd/-
Asst. Registrar (SRICCE)**

Copy to:

- 1) All Heads of the Departments, NIT Rourkela for publication on Departmental Notice Boards.
- 2) Prof. Tarapada Roy, with a request to give wide publicity to advertisement.
- 3) Head of the Department, Mechanical Engineering
- 4) Project file