

About NIT Rourkela

The course will be organized at the Department of Electronics & communication Engineering, National Institute of Technology (NIT), Rourkela. It is one of the premier national level institutions for technical education in the country and is funded by the Government of India. It is situated at the eastern end of Rourkela steel city, beyond Sector-1 over an area of 262 hectares of land. NIT Rourkela has fourteen academic departments which offer B.Tech, M.Tech, MTech (Research) and Ph. D programs in various areas of engineering and technology.

About Enixs Technology India Pvt Ltd

Enixs Technology India Pvt Ltd, is one of the Fastest Growing Advanced Electronic Equipment Suppliers in India. Enixs is engaged in the Development of New Electronic Products and Manufacturing Processes and has Operational Centres at Chennai, Bangalore, Hyderabad, Pune and New Delhi. Enixs is an Impressive Firm, Renowned for its High Standard and Quality Business. Enixs is Functioning with the Motto of "Tech Hope for Third World Countries" and with the Vision of Technology Establishment throughout the Globe. As a Credit to its Performance, Enixs Quickly became One of the Most Popular and Well – Respected Supplier of Embedded, VLSI and Test & Measurement Equipment in Major parts of India and Very Significantly it has become the University Programme Channel Partner for ALTERA Semiconductors USA, Promising World Leader of FPGA Manufacturers. Currently Enixs is functioning with an Excellent R&D Infrastructure Facilities and Technically Skilled Team of Engineers.

About the Department

The Electronics and Communication Engg. Dept. offers two B. Tech. programs (i) Electronics and Communication Engg. (ii) Electronics & Instrumentation Engg. In addition two M. Tech. Programs on (i) Telematics and Signal Processing (ii) VLSI Design and Embedded System are also offered by the department. The Dept. also offers M. Tech(R) and Ph. D. programs in various specializations such as DSP, communication, VLSI Design, Soft Computing and Instrumentation Engg. The Dept. has earned name in imparting effective education, research and training in these areas. The dept. also collaborates with Industry, Academic and Professional bodies and actively engaged in a number of technology development sponsored projects.

Motivation behind the workshop

VLSI Applications form the key to many emerging systems of modern technology. India has a rich source of manpower in Electronics in general and Microelectronics in particular. With the advent of FPGAs we have entered the era of reconfigurable computing. FPGAs act as a sort of system-level silicon super-glue, sitting at the periphery of the circuit board making anything to connect to anything else, so that we could adapt on the fly and change our design to leverage any new, improved component without having to abandon the rest of the design. Through this workshop we try to provide interesting insights into the world of FPGAs. The workshop will provide an understanding of Design-implementation flow involved in FPGA Design and Implementation using ALTERA EDA Tools. Lab exercises will provide the participants with hands-on tool usage experience under the guidance of expert instructors from ALTERA and ENIXS reinforcing the design and implementation steps covered in the lecture.

REGISTRATION FORM

ONE DAY PROFESSOR'S WORKSHOP ON FPGA SYSTEM DESIGN USING ALTERA TOOLS

September 19, 2009

Name :

Designation :

Qualification :

Organization:

Address for Correspondance:

Phone :

E-mail :

Signature of the candidate

Whether Accommodation Needed : Yes / No

Signature of the Head

Workshop Schedule

- 09.30 am Inauguration
- 10.00 am Introduction to ASIC & FPGA
- 11.00 am Tea Break
- 11.15 am Case study on FPGA Application
- 12.00 pm Introduction to Altera Tools
- 12.30 pm Lunch
- 01.30 pm Hands-on Implementation using Quartus II Software
- 02.30 pm Tea brake
- 02.45 pm Hands-on Implementation using SOPC Builder & NIOS II Processor
- 05.00 pm Valedictory Function

Lucky Draw at the End of Workshop

Prizes: Altera Development Kit, VLSI books

Who Should Attend?

The workshop is exclusively meant for teaching faculty in the engineering colleges. No TA/DA will be paid for the participants.

Resource Persons

Experts from ALTERA and Enixs Technology India Pvt Ltd.

Registration Fee

No registration fee will be charged. No TA and DA will be given to the participants.

Outstation participants may be provided with shared accommodation facility in the Institute Guest House on prior request. Participants have to bear the boarding and lodging cost.

The duly filled application forms are to be sent to:

CORRESPONDENCE

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Coordinators

Prof. K. K. Mahapatra
Prof. Bibhudatta Sahoo



Sponsored by



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